

Refine Search

Search Results -

Term	Documents
(11 NOT 10).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	4
(L11 NOT L10).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	4

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L12

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<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
side by side			
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L12</u>	L11 not l10	4	<u>L12</u>
<u>L11</u>	l1 near25 (translat\$5 or convert\$4 or conversion\$1) near12 (match\$5 or hit\$1 or exist\$3 or already or previous\$3 or before)	24	<u>L11</u>
<u>L10</u>	l1 near15 (translat\$5 or convert\$4 or conversion\$1) near12 (match\$5 or hit\$1 or exist\$3 or already or previous\$3 or before)	20	<u>L10</u>
<u>L9</u>	L7 and dynamic\$7 near8 (translat\$5 or convert\$4 or conversion\$1)	22	<u>L9</u>
<u>L8</u>	L7 and dynamic\$7	119	<u>L8</u>
<u>L7</u>	L6 and cache	142	<u>L7</u>
<u>L6</u>	L5 and (table\$1 or look near3 up)	562	<u>L6</u>
<u>L5</u>	L1 and (translat\$5 or convert\$4 or conversion\$1) near18 (minimiz\$5 or reduc\$4 or avoid\$5 or bypass\$5 or skip\$4 or eliminat\$3)	663	<u>L5</u>
<u>L4</u>	L3 and cache	5033	<u>L4</u>

<u>L3</u>	L2 and (table\$1 or look near3 up)	106183	<u>L3</u>
<u>L2</u>	L1 (translat\$5 or convert\$4 or conversion\$1) near18 (minimiz\$5 or reduc\$4 or avoid\$5 or bypass\$5 or skip\$4 or eliminat\$3)	306342	<u>L2</u>
<u>L1</u>	(append\$4 or attach\$5 or flag\$5 or tag\$5 or embed\$5 or insert\$5) near8 source near4 (cod\$3 or program\$1 or instruction\$1)	8100	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Term	Documents
(7 AND 5).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	21
(L7 AND L5).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	21

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<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L12</u>	L7 and l5	21	<u>L12</u>
<u>L11</u>	L7 and l4	15	<u>L11</u>
<u>L10</u>	L7 and l3	7	<u>L10</u>
<u>L9</u>	L7 and l2	8	<u>L9</u>
<u>L8</u>	L7 and l1	8	<u>L8</u>
<u>L7</u>	L6 and (look\$3 near3 up or table\$1) near12 cach\$5	241	<u>L7</u>
<u>L6</u>	(convert\$7 or conversion\$1 or translat\$7 or compil\$7) near12 (program\$1 or source or cod\$3 or instruction\$1 or language\$1) near12 (flag\$5 or tag\$5)	5682	<u>L6</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L5</u>	(717/134-149)![CCLS]	2875	<u>L5</u>
<u>L4</u>	(712/209-213)[CCLS]	1134	<u>L4</u>
<u>L3</u>	(703/26)[CCLS]	341	<u>L3</u>

L2 (703/24-28)[CCLS]

1347 L2

L1 (703/24-28)![CCLS]

1347 L1

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Search Results -

Term	Documents
(27 NOT 19).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	1
(L27 NOT L19).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	1

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side by side			
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L31</u>	L27 not l19	1	<u>L31</u>
<u>L30</u>	L29 not l19	12	<u>L30</u>
<u>L29</u>	L25 and l11	12	<u>L29</u>
<u>L28</u>	L25 and l10	67	<u>L28</u>
<u>L27</u>	L25 and l17	20	<u>L27</u>
<u>L26</u>	L25 and l16	43	<u>L26</u>
<u>L25</u>	L24 and cache	253	<u>L25</u>
<u>L24</u>	L23 and dynamic\$5 near10 (conversion\$1 or convert\$3 or translat\$7 or compil\$7)	392	<u>L24</u>
<u>L23</u>	compil\$7 and (match\$5 or exist\$3 or by near1 pass\$3 or look near2 up or skip\$4 or avoid\$5 or prevent\$3 or releav\$5 or minimiz\$6 or reduc\$4) near12 (conversion\$1 or convert\$3 or translat\$7) and tag\$4	5524	<u>L23</u>

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L22</u>	115 and 111	12	<u>L22</u>
<u>L21</u>	115 and 110	63	<u>L21</u>
<u>L20</u>	118 not 119	20	<u>L20</u>
<u>L19</u>	115 and 117	19	<u>L19</u>
<u>L18</u>	115 and 116	39	<u>L18</u>
<u>L17</u>	(712/24-217)[CCLS]	6185	<u>L17</u>
<u>L16</u>	(712/2-300)[CCLS]	12204	<u>L16</u>

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L15</u>	19 and dynamic\$5 near10 (conversion\$1 or convert\$3 or translat\$7 or compil\$7)	229	<u>L15</u>
<u>L14</u>	19 and dynamic\$5	642	<u>L14</u>

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L13</u>	19 and 111	14	<u>L13</u>
<u>L12</u>	19 and 110	99	<u>L12</u>
<u>L11</u>	(717/107,108,116,122, 136-148)![CCLS]	1119	<u>L11</u>
<u>L10</u>	(717/101-178)![CCLS]	10912	<u>L10</u>

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L9</u>	L8 and cache	731	<u>L9</u>
<u>L8</u>	compil\$7 and (match\$5 or exist\$3 or by near1 pass\$3 or look near2 up or skip\$4 or avoid\$5 or prevent\$3) near8 (conversion\$1 or convert\$3 or translat\$7) and tag\$4	4380	<u>L8</u>
<u>L7</u>	L6 not 14	4	<u>L7</u>
<u>L6</u>	L5 and table\$1	59	<u>L6</u>
<u>L5</u>	compil\$7 near45 (match\$5 or exist\$3 or by near1 pass\$3 or look near2 up or skip\$4 or avoid\$5 or prevent\$3) near8 (conversion\$1 or convert\$3 or translat\$7) and tag\$4	80	<u>L5</u>
<u>L4</u>	L3 and table\$1	55	<u>L4</u>
<u>L3</u>	compil\$7 near45 (match\$5 or exist\$3 or by near1 pass\$3 or look near2 up) near8 (conversion\$1 or convert\$3 or translat\$7) and tag\$4	75	<u>L3</u>
<u>L2</u>	compil\$7 and (match\$5 or exist\$3 or by near1 pass\$3 or look near2 up) near8 (conversion\$1 or convert\$3 or translat\$7) and tag\$4	1601	<u>L2</u>
<u>L1</u>	compil\$7 and (match\$5 or exist\$3 or by near1 pass\$3 or look near2 up) near8 (conversion\$1 or convert\$3 or translat\$7)	4721	<u>L1</u>

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» Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **CPU-independent assembler in an FPGA**
Achery, G.; Trinitis, C.; Buchty, R.;
[Field Programmable Logic and Applications, 2005. International Conference on](#)
24-26 Aug. 2005 Page(s):519 - 522
Digital Object Identifier 10.1109/FPL.2005.1515775
[AbstractPlus](#) | Full Text: [PDE\(154 KB\)](#) IEEE CNF
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- ☐ 2. **Compiler-directed physical address generation for reducing dTLB power**
Kadayif, I.; Nath, P.; Kandemir, M.; Sivasubramaniam, A.;
[Performance Analysis of Systems and Software, 2004. IEEE International Symposium on - ISPASS](#)
2004 Page(s):161 - 168
Digital Object Identifier 10.1109/ISPASS.2004.1291368
[AbstractPlus](#) | Full Text: [PDE\(1737 KB\)](#) IEEE CNF
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- ☐ 3. **A fast logic simulator using a look up table cascade emulator**
Nakahara, H.; Sasao, T.; Matsuura, M.;
[Design Automation, 2006. Asia and South Pacific Conference on](#)
24-27 Jan. 2006 Page(s):7 pp.
Digital Object Identifier 10.1109/ASPDAC.2006.1594729
[AbstractPlus](#) | Full Text: [PDE\(347 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **Thread-Shared Software Code Caches**
Bruening, D.; Kiriansky, V.; Garnett, T.; Banerji, S.;
[Code Generation and Optimization, 2006. CGO 2006. International Symposium on](#)
26-29 March 2006 Page(s):28 - 38
Digital Object Identifier 10.1109/CGO.2006.36
[AbstractPlus](#) | Full Text: [PDE\(360 KB\)](#) IEEE CNF
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- ☐ 5. **Generating physical addresses directly for saving instruction TLB energy**
Kadayif, I.; Sivasubramaniam, A.; Kandemir, M.; Kandiraju, G.; Chen, G.;
[Microarchitecture, 2002. \(MICRO-35\) Proceedings. 35th Annual IEEE/ACM International Symposium](#)
18-22 Nov. 2002 Page(s):185 - 196
Digital Object Identifier 10.1109/MICRO.2002.1176249

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6. The use of prediction for accelerating upgrade misses in cc-NUMA multiprocessors

Acacio, M.E.; Gonzalez, J.; Garcia, J.M.; Duato, J.;

[Parallel Architectures and Compilation Techniques, 2002. Proceedings. 2002 International Conference](#)
22-25 Sept. 2002 Page(s):155 - 164

Digital Object Identifier 10.1109/PACT.2002.1106014

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